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Quick Reference For Verilog HDL. 1. 1.0 Lexical Elements. The Language Is Case Sensitive And All The Keywords Are Lower Case. White Space, Namely, Spaces, Tabs And New-lines Are Ignored. Jun 1th, 2020

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Was Infeasible. The Modern Design Tools At The Time Were Targeted At Custom
Integrated Circuits, Which Are Cost- And Time-prohibitive To Implement In A
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And Design Tools Allowed The Modern Digital Design Approach To Be Sep 1th, 2020

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